

FIG. 1

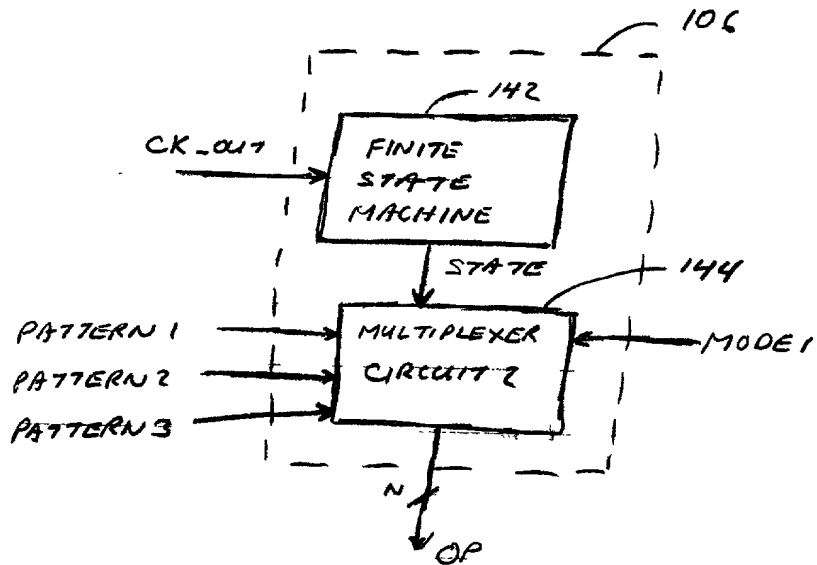


FIG. 3

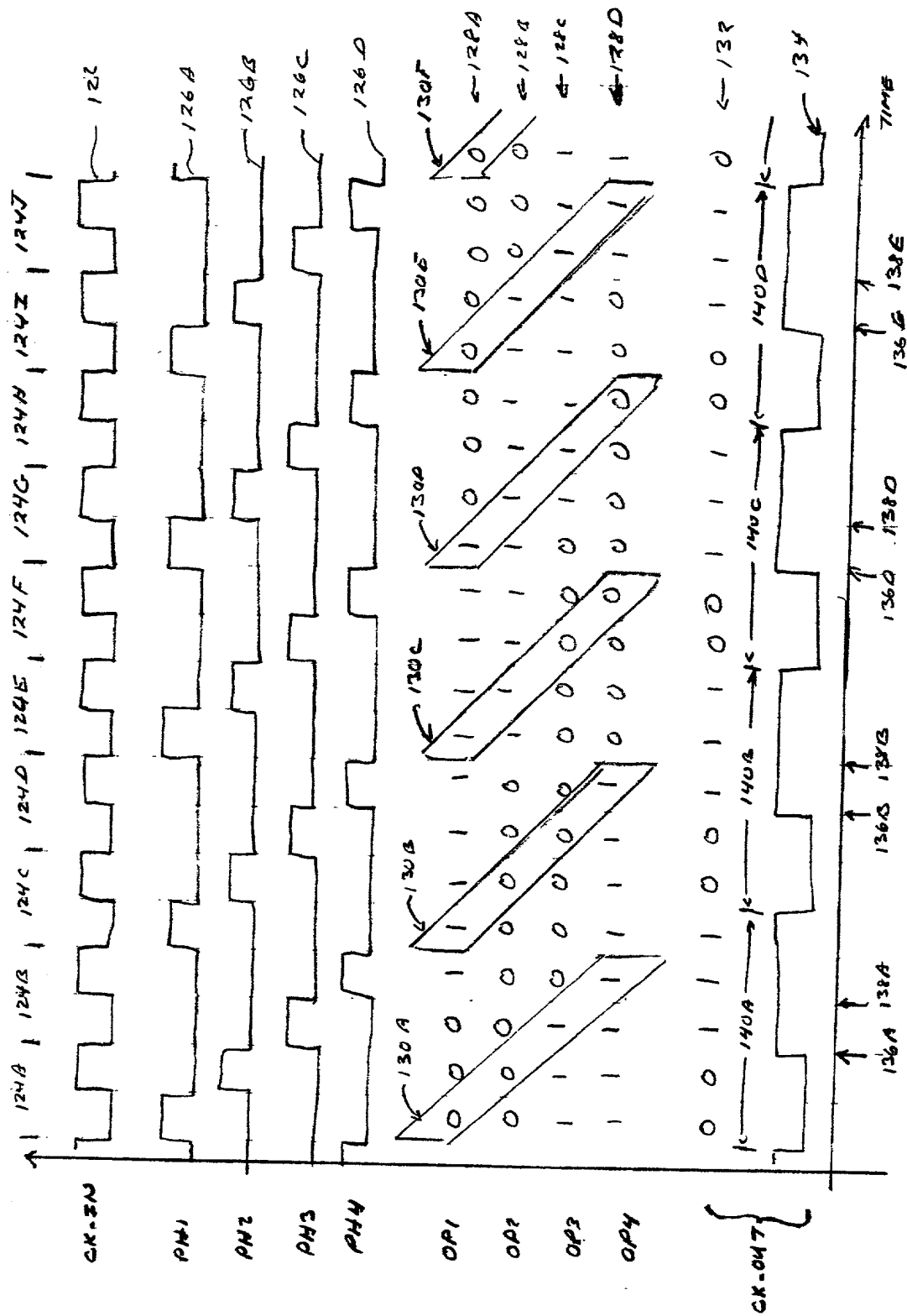


FIG. 2

Timing diagram for the 74163 counter. The diagram shows four outputs: CK-IN, OP1, OP2, OP3, OP4, and CK-OUT over time. CK-IN is a square wave with period K=124. CK-OUT is a square wave with period K=150. OP1, OP2, OP3, and OP4 are outputs of a 4-bit counter. The counter state is shown as a sequence of 0s and 1s: 0 1 0 1 0 1 0 1 0. The counter is labeled 74163 and has a 'TIME' axis.

Timing diagram showing three signals over time:

- CK-ZN**: A square wave signal.
- CK-OUT**: A square wave signal with a period of 154. The signal is high for 154 units of time.
- CK-OUT2**: A square wave signal with a period of 158. The signal is high for 158 units of time.

The signals are labeled with their respective values: CK-ZN, CK-OUT, and CK-OUT2. The time axis is labeled 'TIME'.

FIG. 5

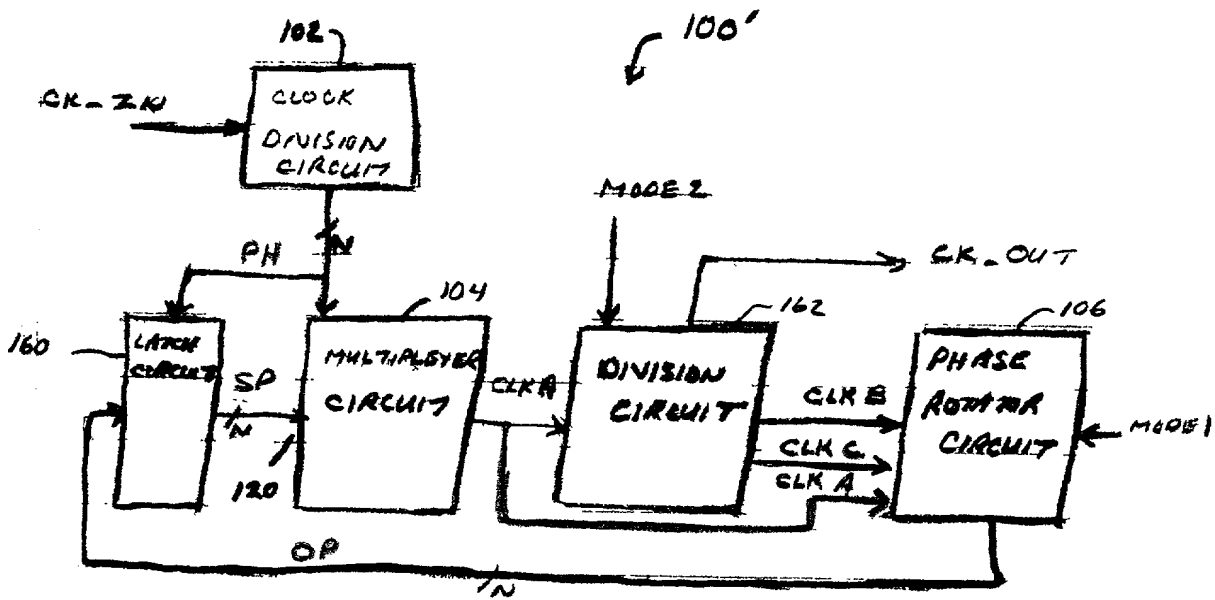


FIG. 6

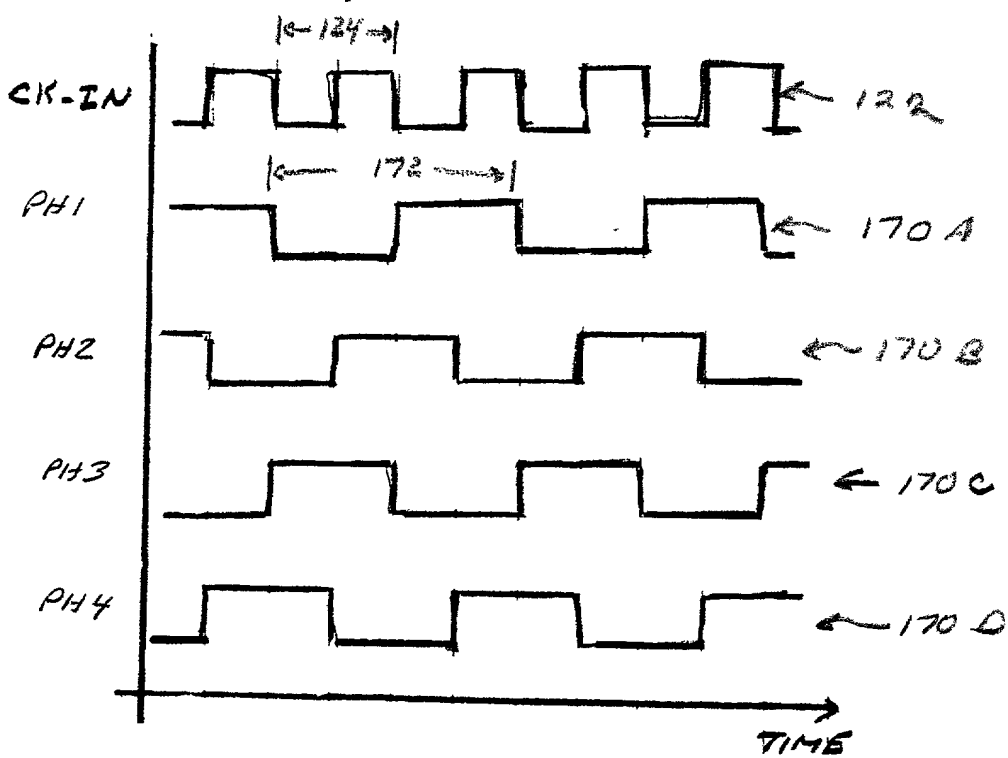


FIG. 8

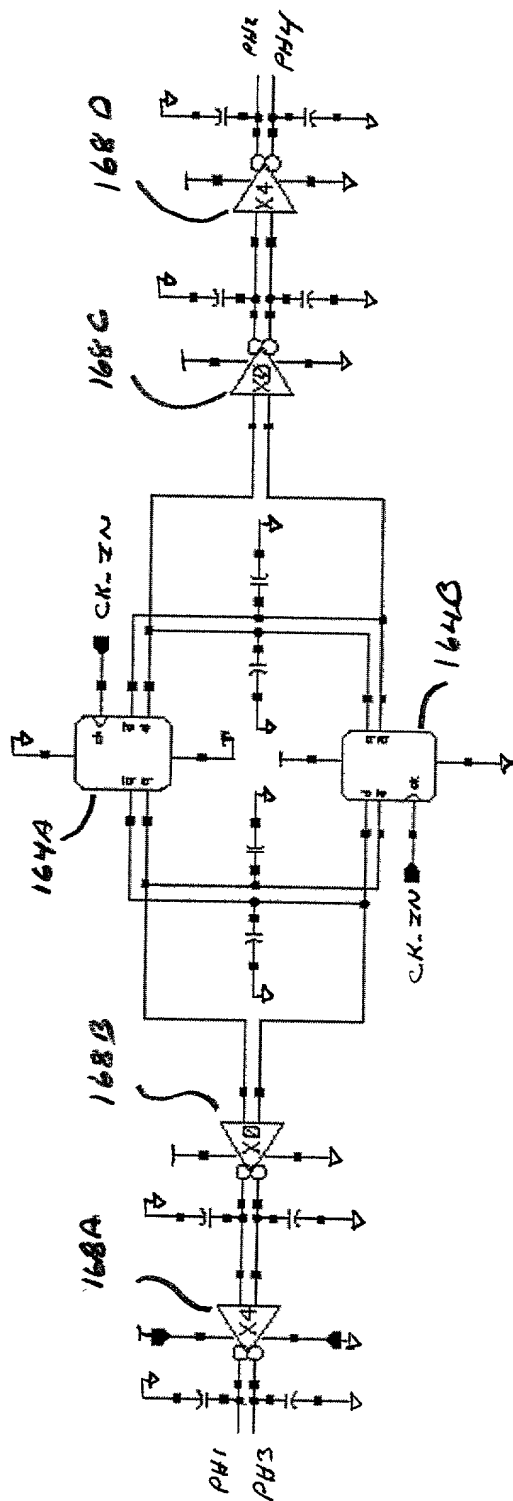


FIG. 7

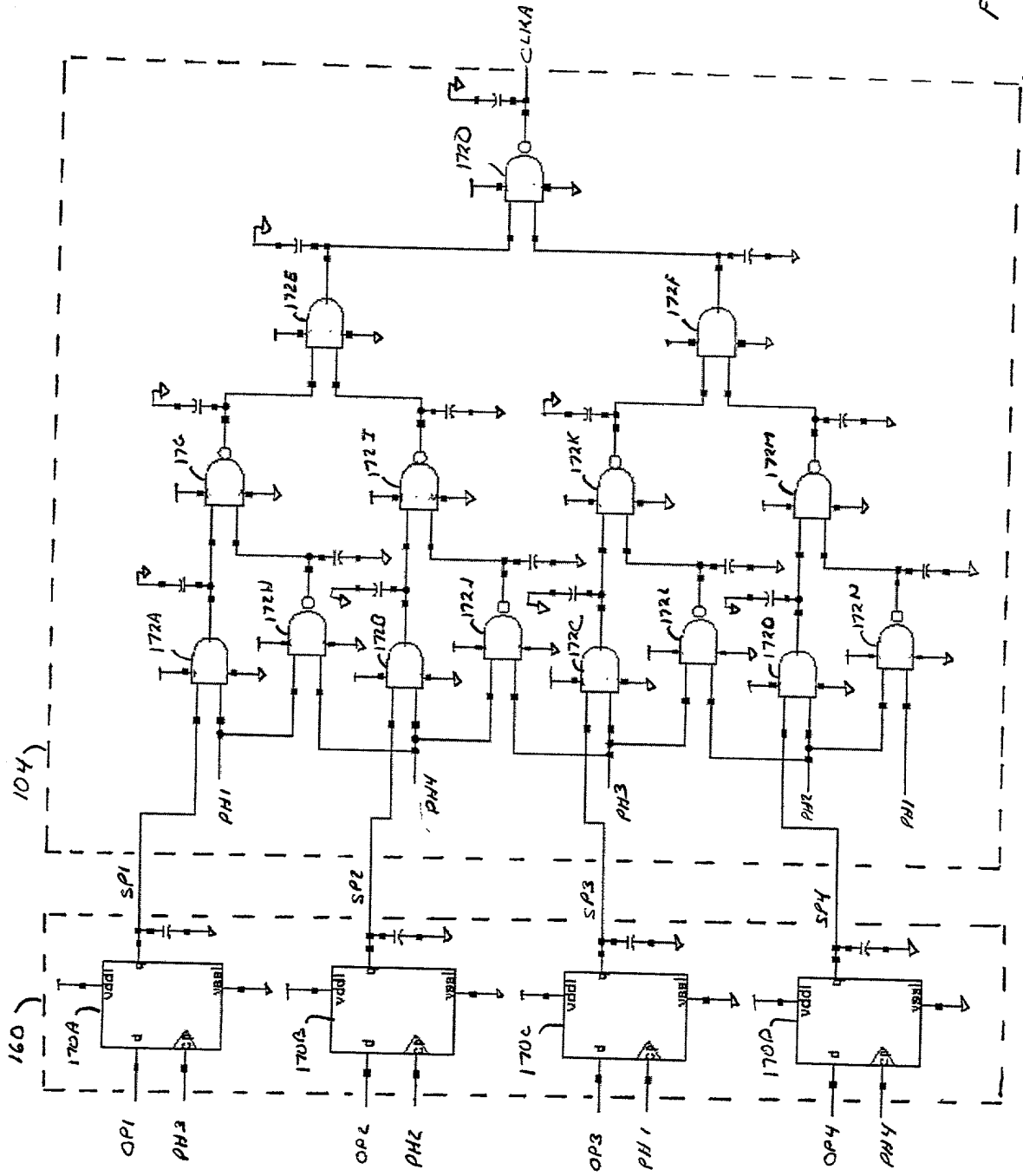


FIG. 9



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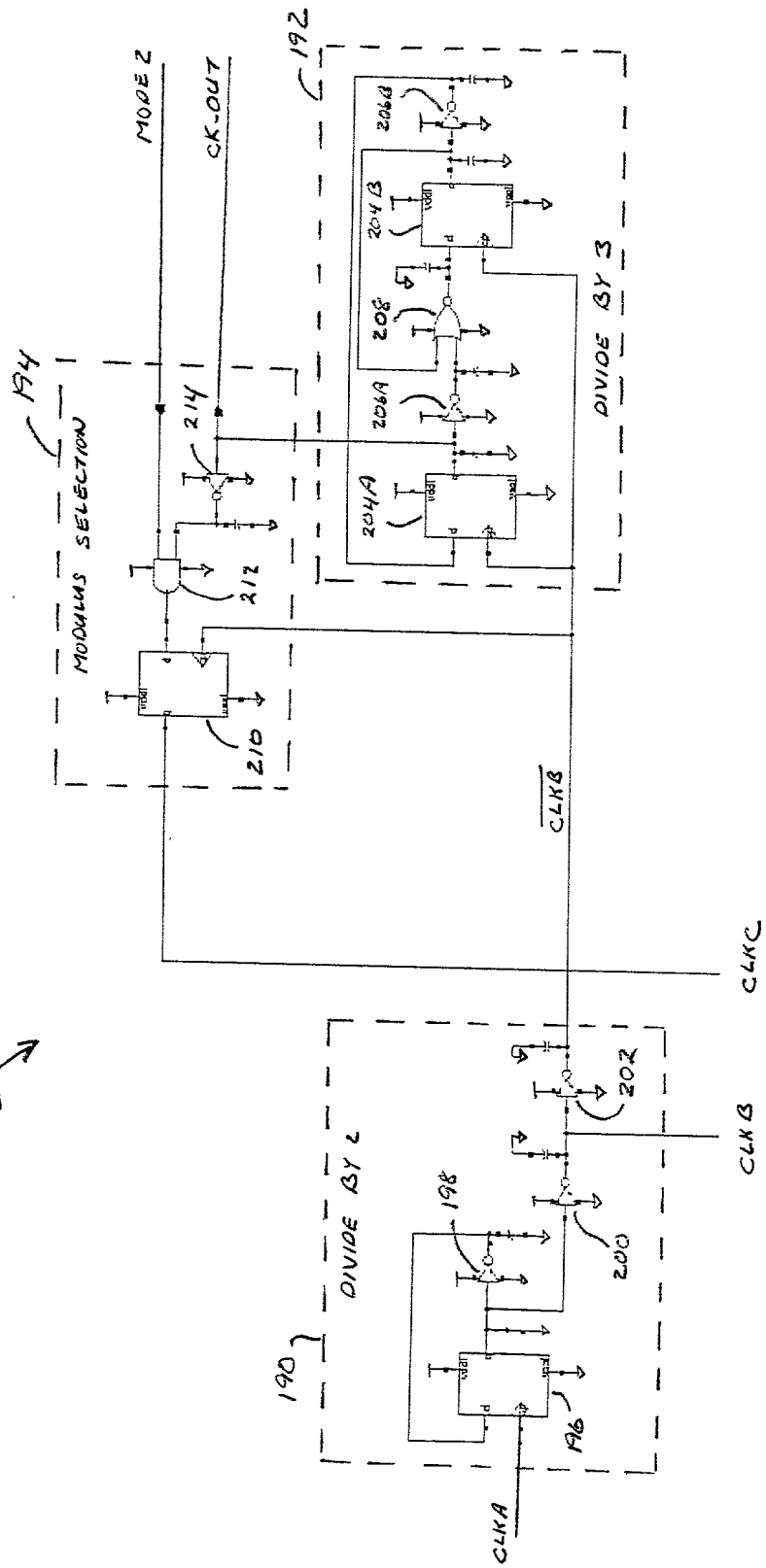


FIG. 11



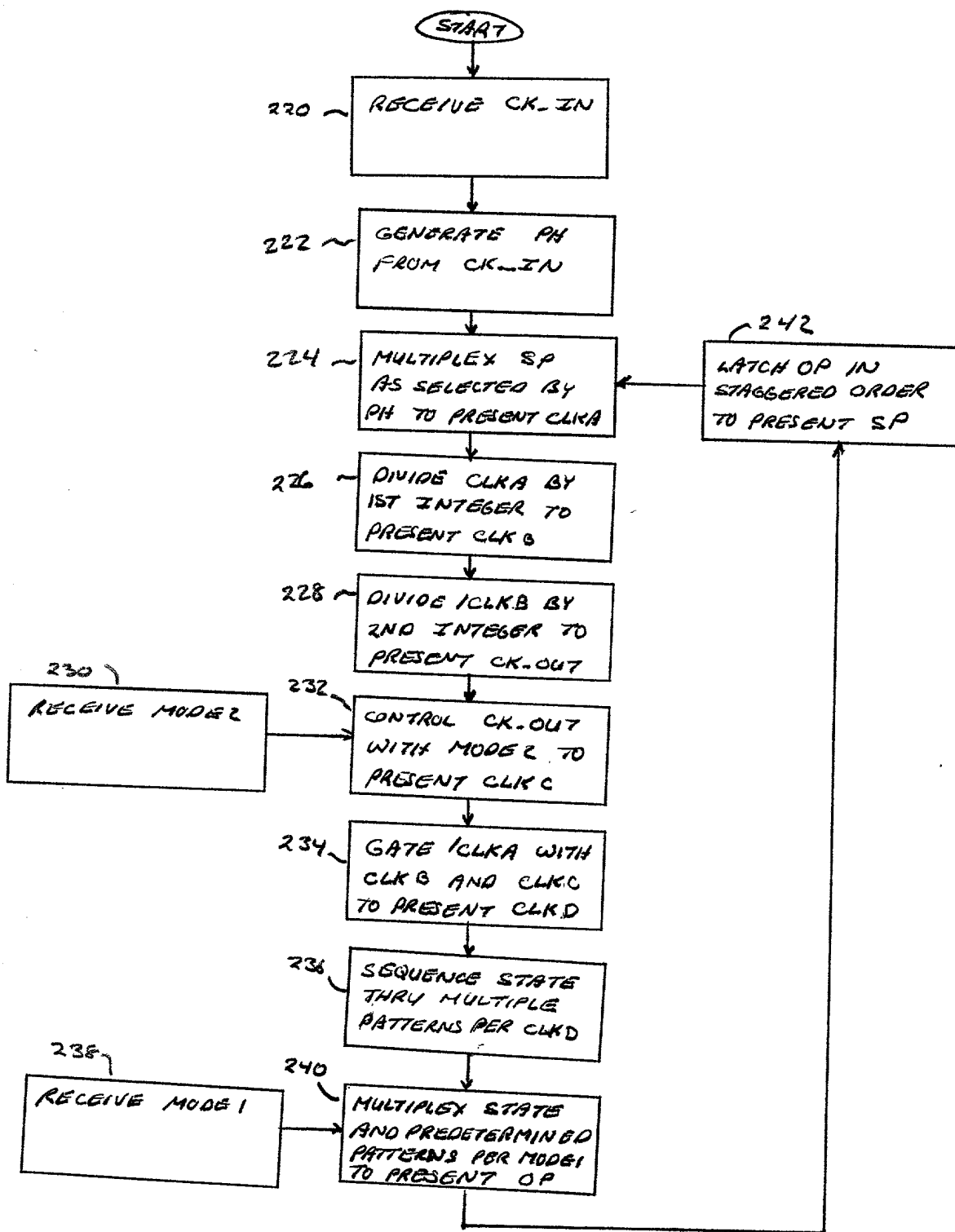


FIG. 12